

Figure 1

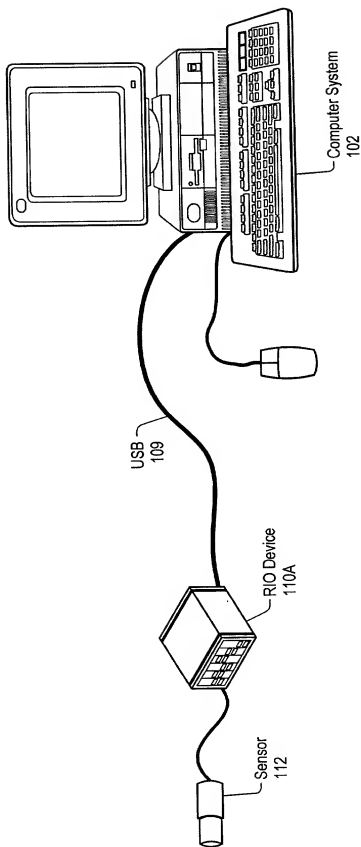


Figure 1A

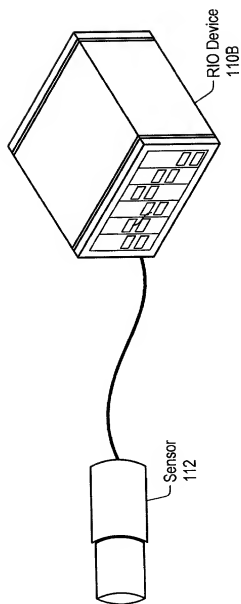


Figure 1B

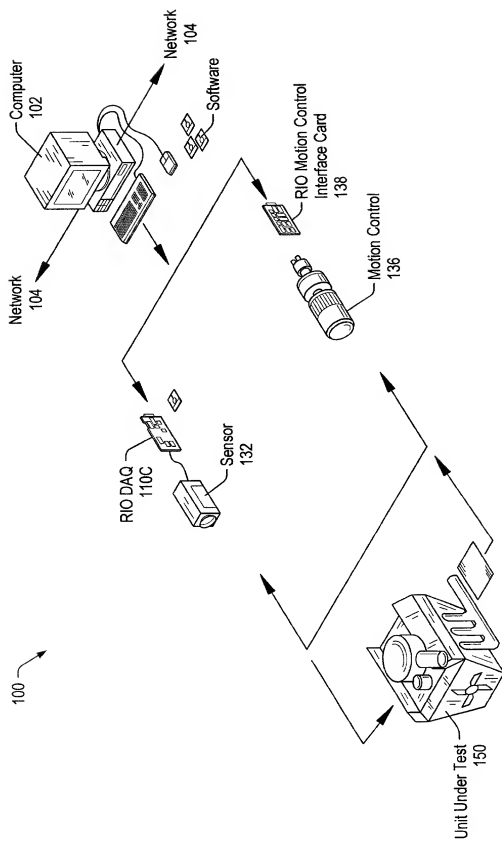


Figure 1C

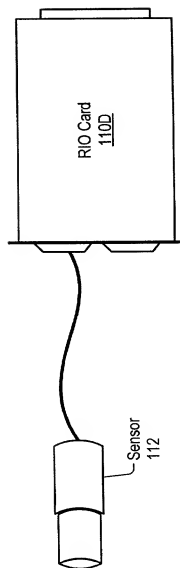


Figure 1D



Figure 1E

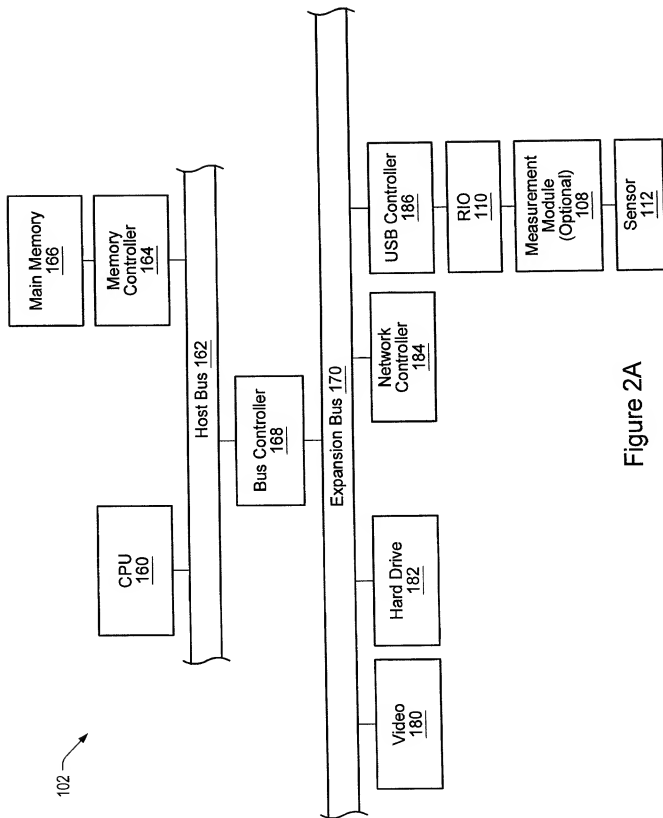


Figure 2A

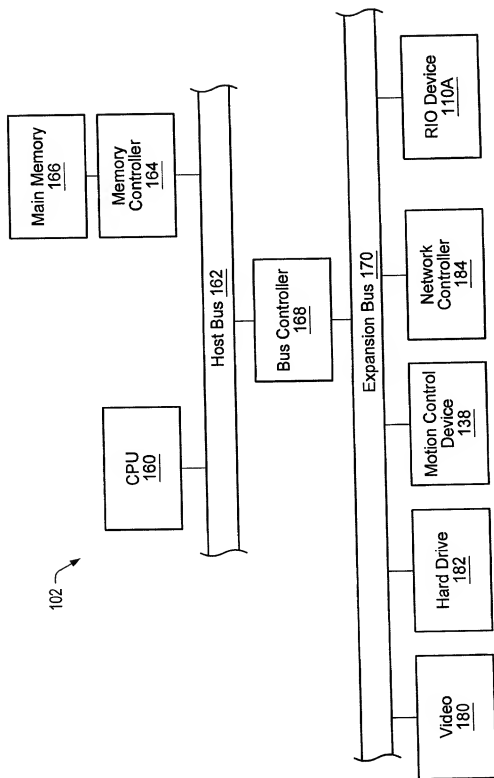


Figure 2B



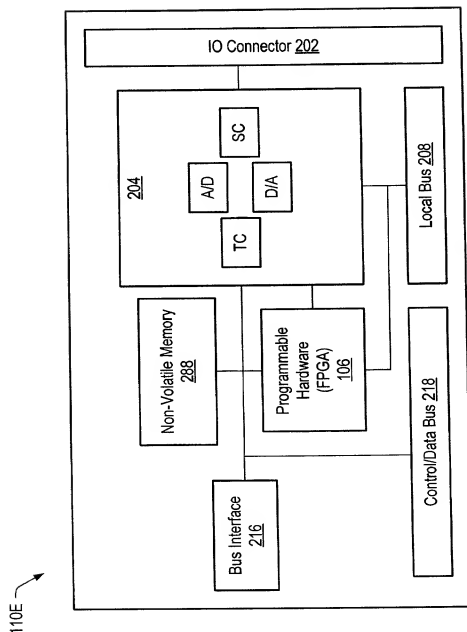


Figure 3A

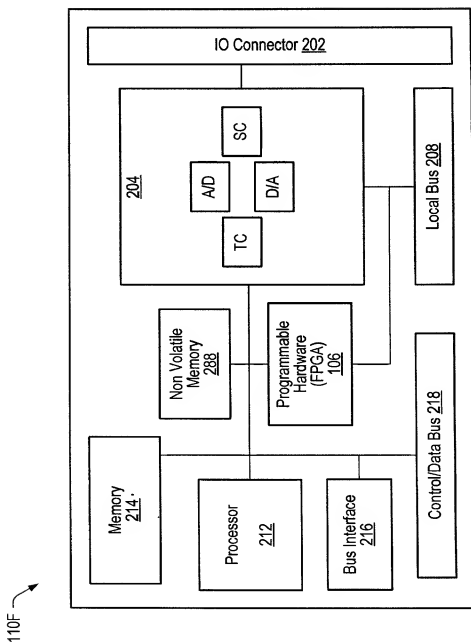


Figure 3B

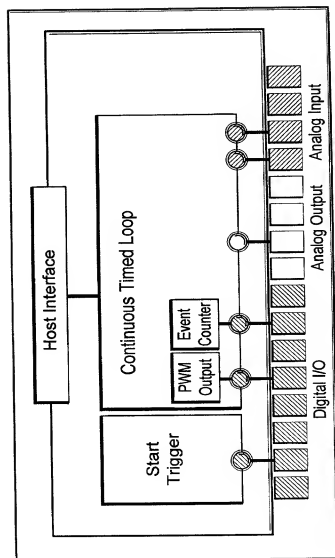


Figure 3C

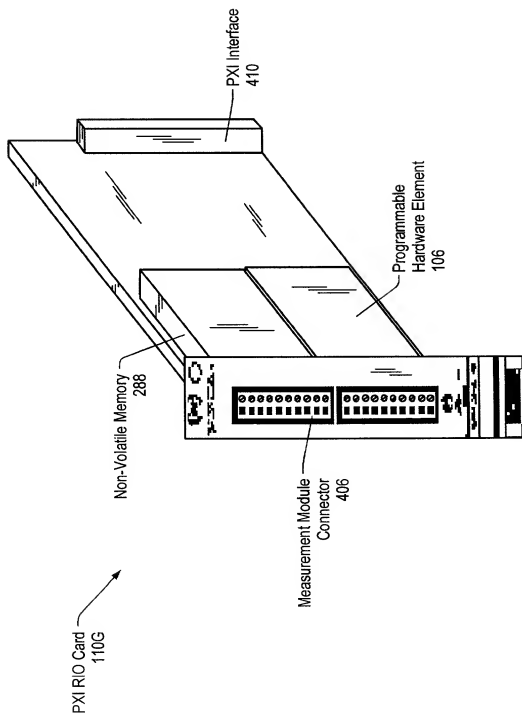


Figure 4

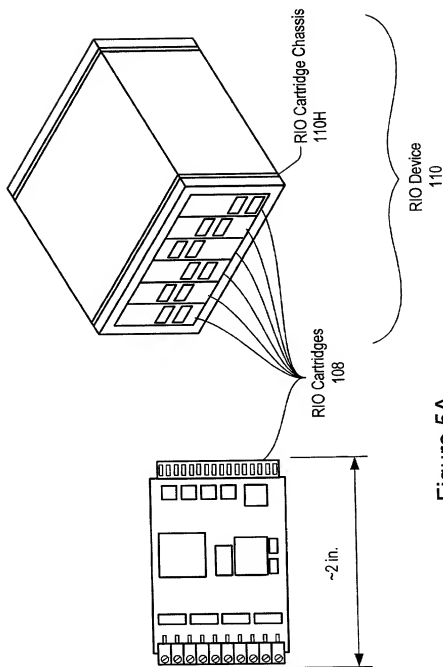


Figure 5A

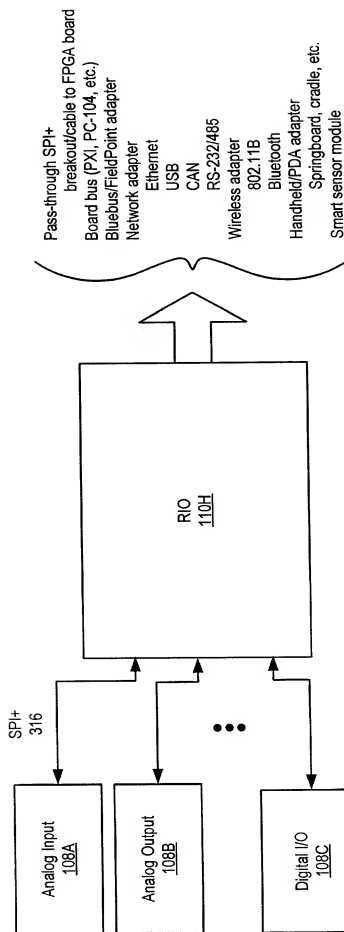


Figure 5B

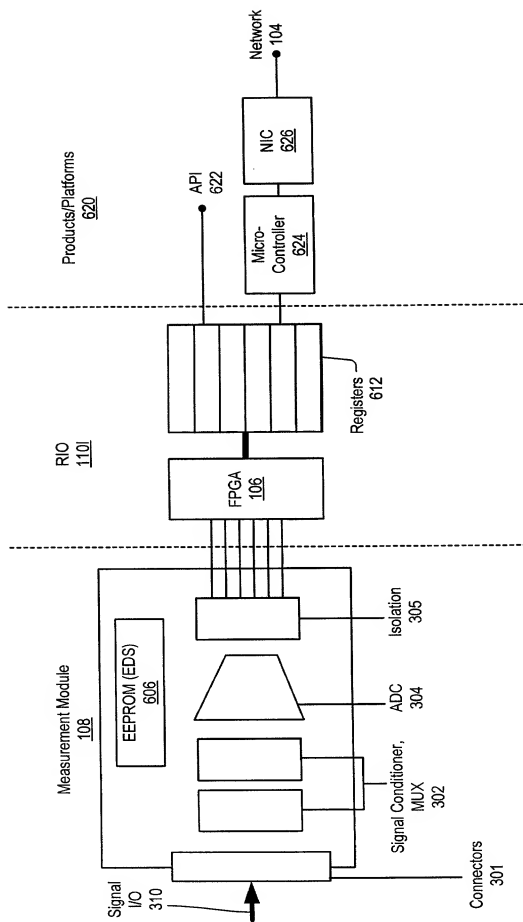


Figure 6

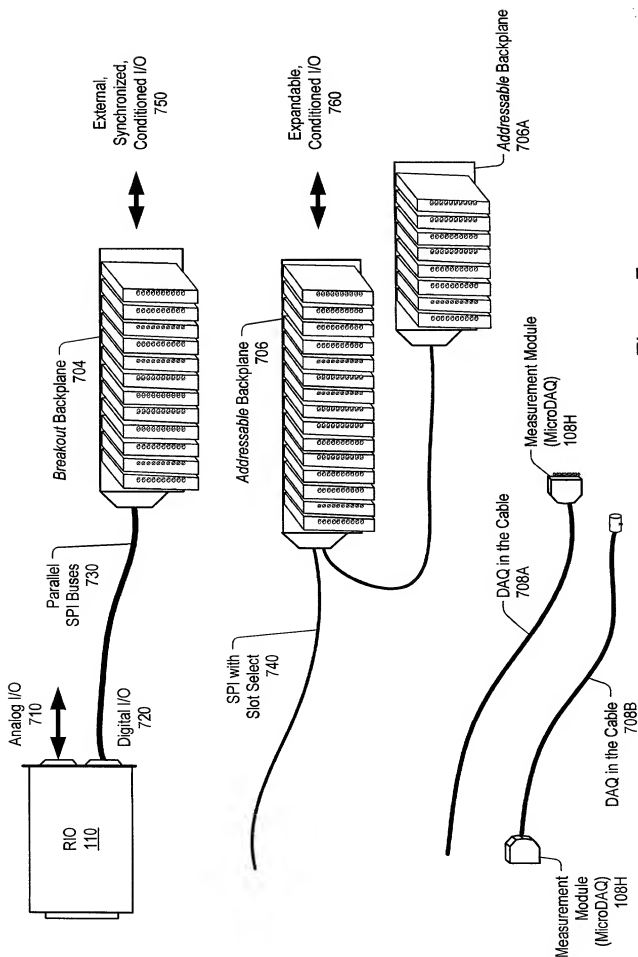


Figure 7



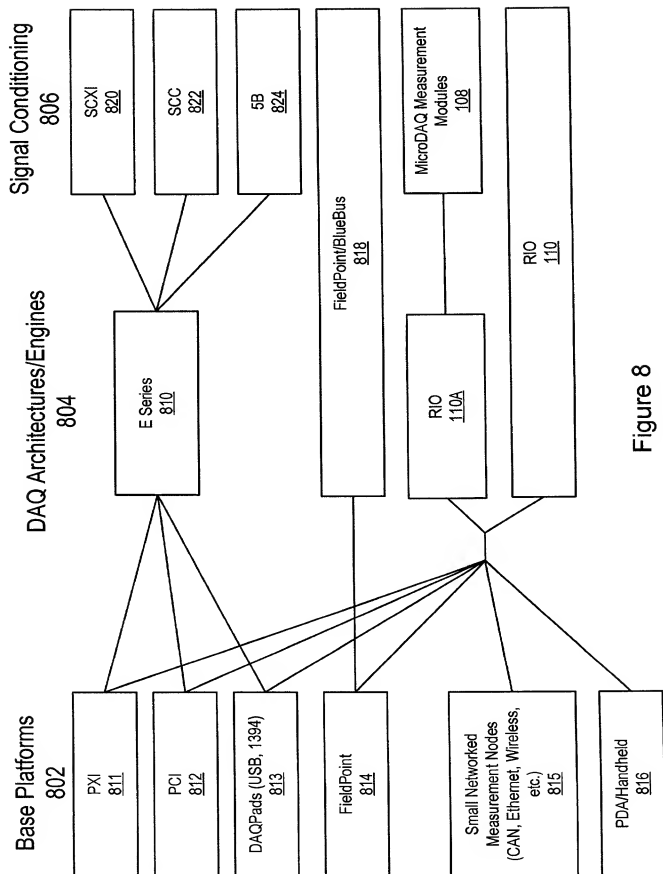


Figure 8

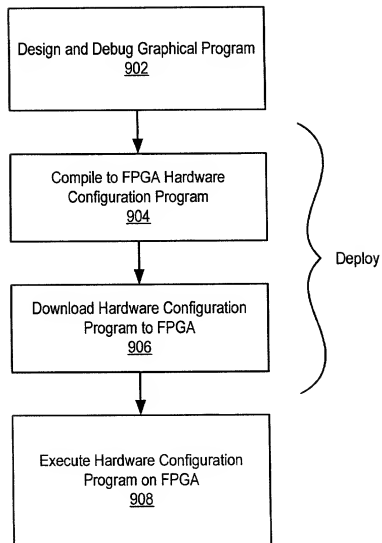


Figure 9

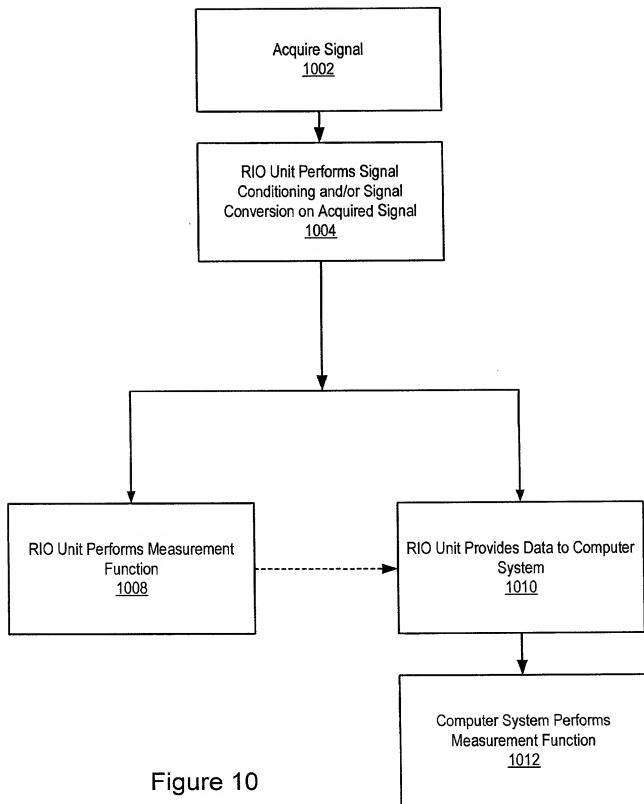


Figure 10

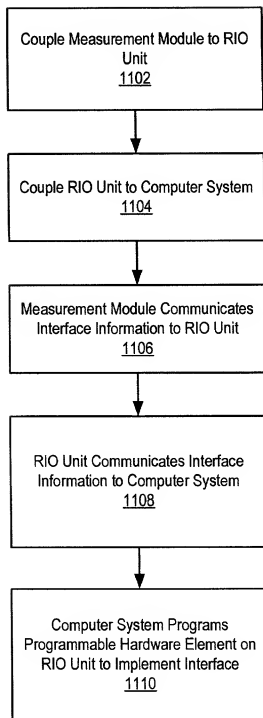


Figure 11A

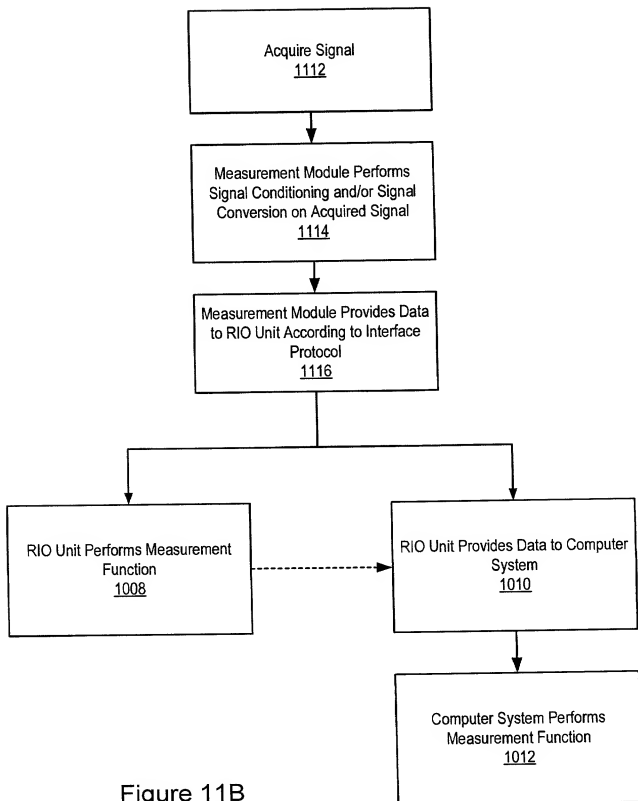


Figure 11B



Figure 12

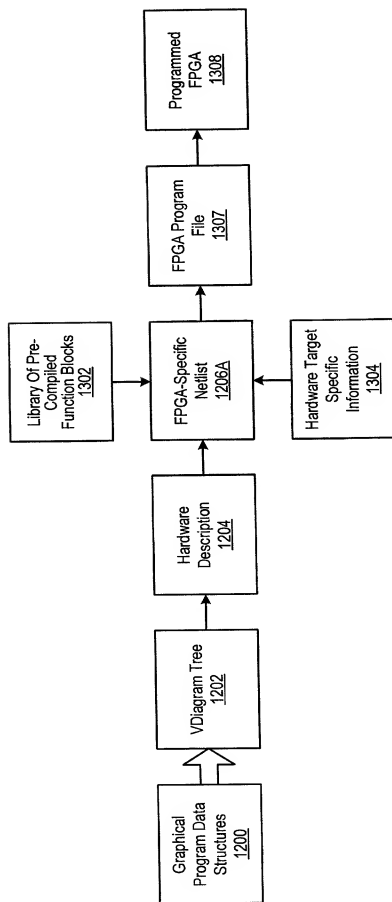


Figure 13

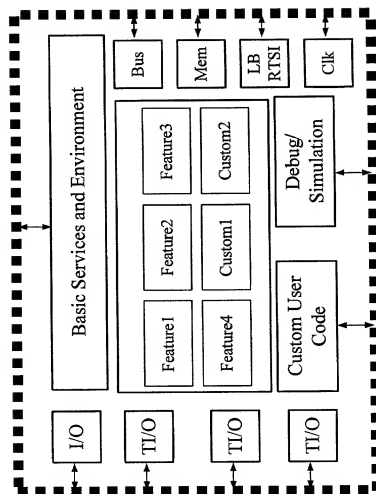


Figure 14



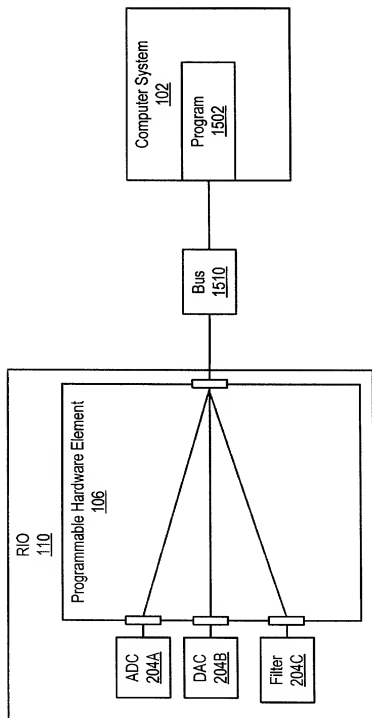


Figure 15

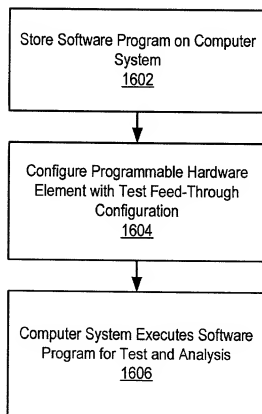


Figure 16

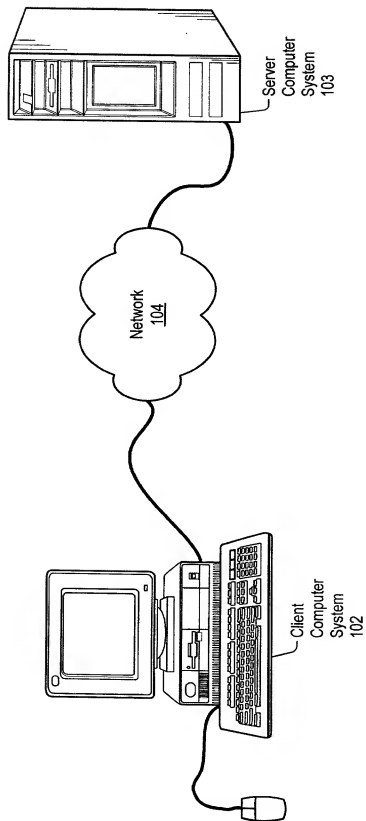


Figure 17

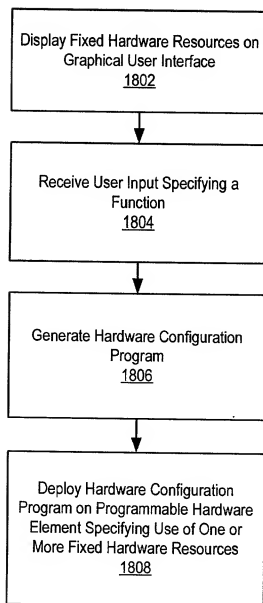


Figure 18

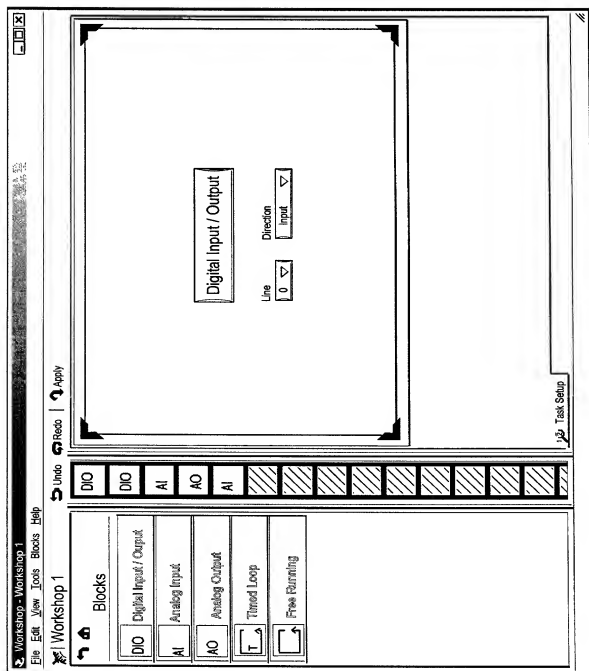


Figure 19A

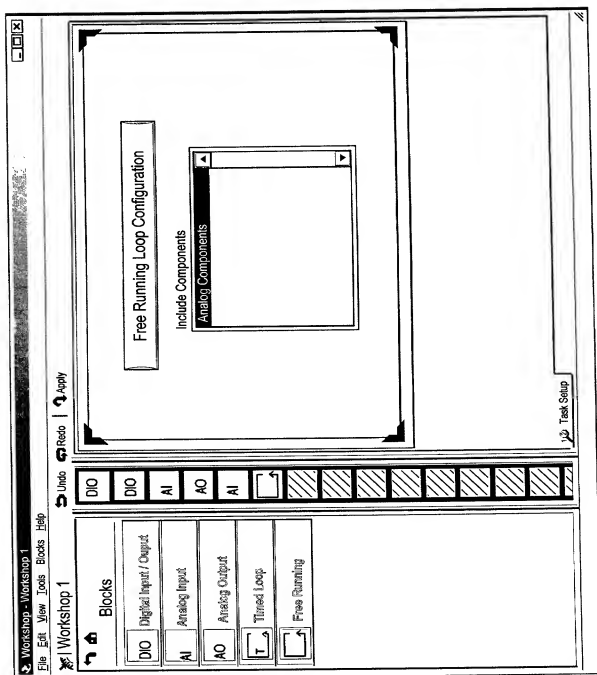
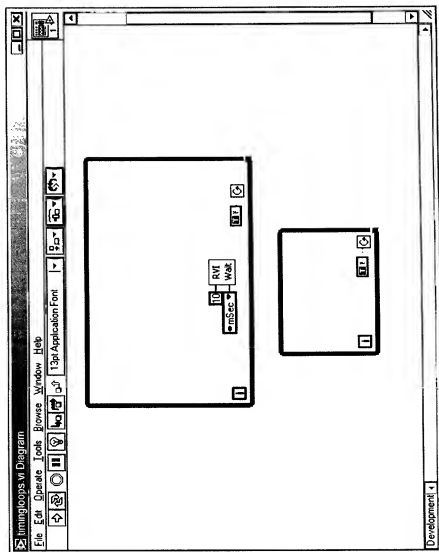
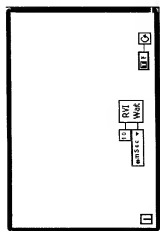


Figure 19B

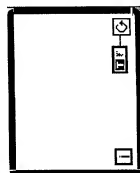


Diagram

Figure 19C



Timed Loop



Free Running Loop

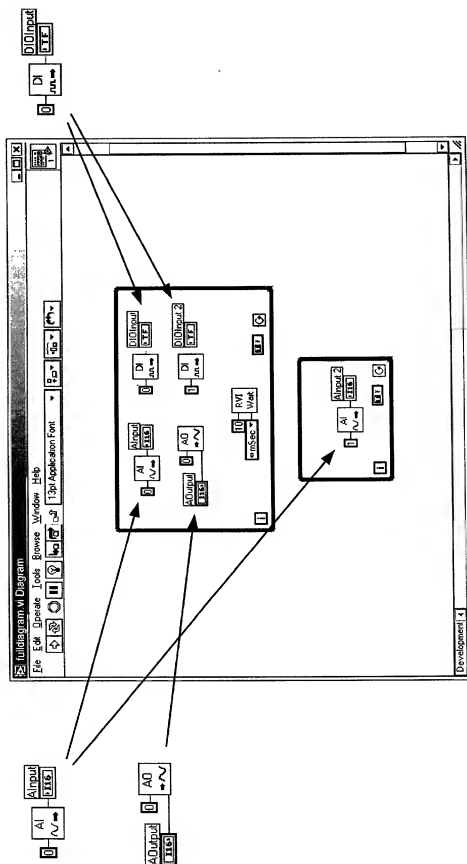


Figure 19D



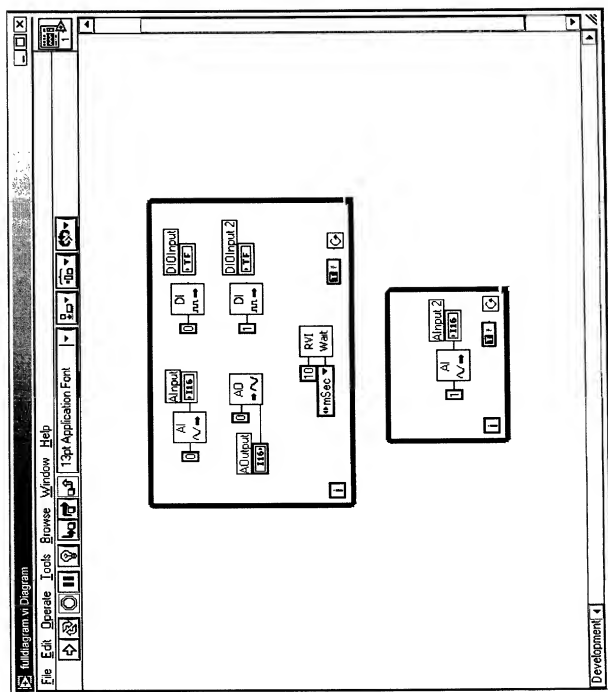


Figure 19E

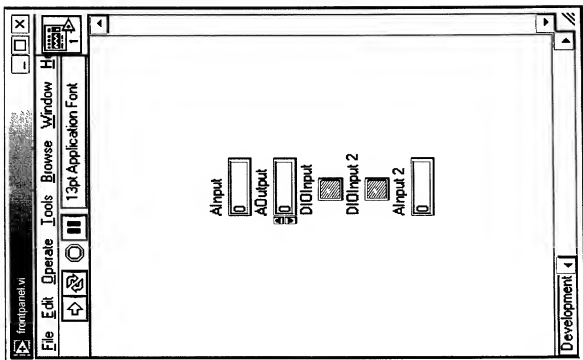


Figure 19F



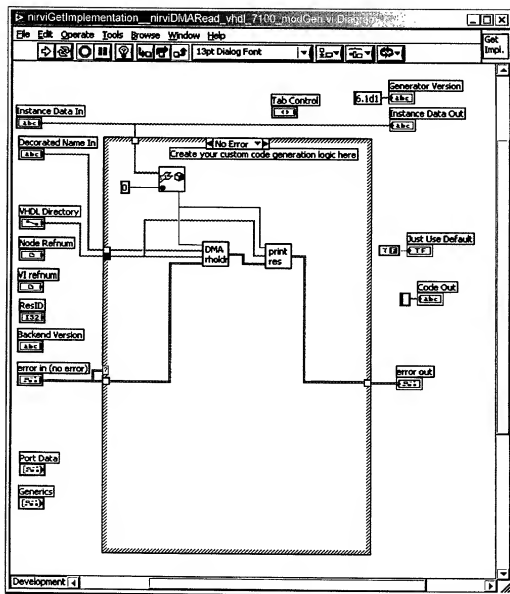


Figure 21

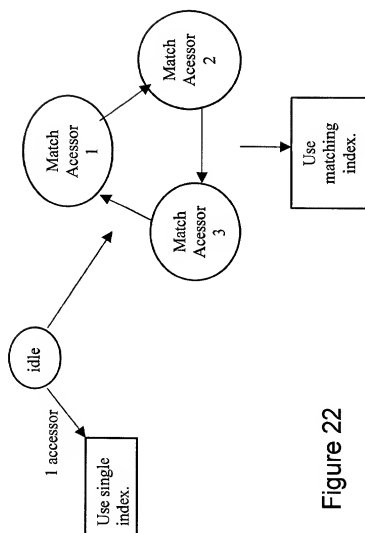
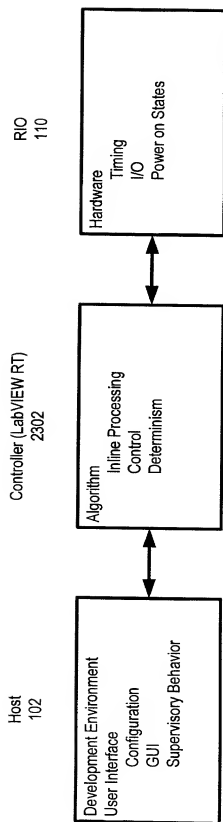


Figure 22



Optional at Runtime

Figure 23A

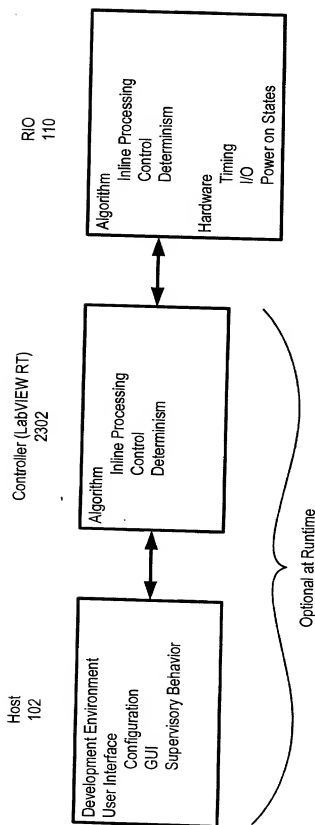


Figure 23B

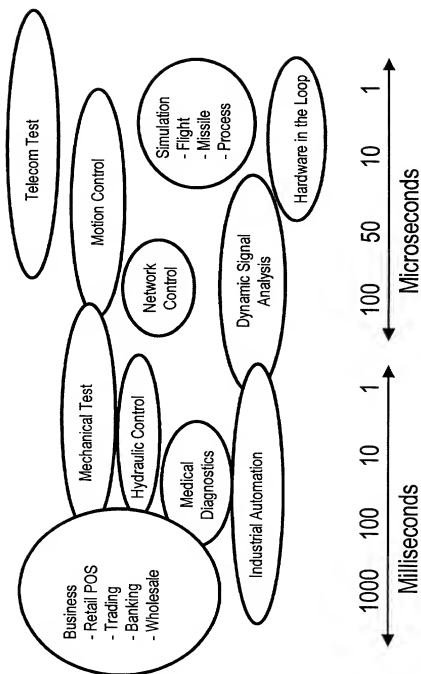


Figure 24